

5                                   **RECONFIGURABLE ANALOG BASEBAND FOR  
A SINGLE-CHIP DUAL-MODE TRANSCEIVER**

**CROSS-REFERENCE TO RELATED APPLICATIONS**

10   This application claims the benefit of the filing date of U.S. Provisional Application  
Serial Number 60/445,683, filed February 7, 2003 entitled "Reconfigurable Analog  
Baseband for A Single Chip Dual Mode Transceiver", which application is incorporated  
herein by reference in its entirety.

15                                   **FIELD OF THE INVENTION**

The invention relates generally to single integrated circuit ("IC") chip implementations of  
radio transceiver systems and more particularly to high-level systems such as Bluetooth  
and 802.11b wireless local area network ("WLAN") systems that can be implemented  
20   on a single integrated circuit (IC) chip using reduced chip area.

**BACKGROUND OF THE INVENTION**

As the wireless market rapidly has grown in recent years, a need has arisen for a single  
wireless transceiver IC that can work with several different wireless protocols, more  
25   particularly Bluetooth and 802.11b. Prior art implementations for standalone Bluetooth  
or 802.11b transceivers are known. Typically, direct-conversion or other RF conversion  
techniques perform a simple frequency translation between RF and baseband. Post-  
conversion, most of the channel-select filtering and amplification is carried out in the  
baseband.

30   In Bluetooth receive (RX) mode, a typically 2.4 GHz RF signal is down-converted to  
2MHz low-IF for channel-select band-pass filtering, limiting, and subsequent band-pass

filtering suppresses harmonics from the previous limiting operation. The resultant signals are then passed to a subsequent GFSK demodulator stage.

Using 802.11b protocol, in wireless-LAN (WLAN) RX mode, RF signals are down-converted to DC for channel-select low-pass filtering. The converted signals are then processed through signal amplitude-controlled variable gain amplifiers (VGA's).

Common analog/digital (A/D) converters may be used for the Bluetooth and 802.11b protocol signals to quantize I/Q outputs for equalization and demodulation. Exemplary prior art design approaches for complex filters suitable for Bluetooth transceiver applications may be found in J. Crols and M. Steyaert, "Low-IF Topologies for High-Performance Analog Front Ends of Fully Integrated Receivers," IEEE Trans. Circuits Syst.-II, vol.45, pp. 269-282, March 1998, herein incorporated by reference.

The most straightforward approach to provide dual functionality found in the prior art appears to be placing a Bluetooth transceiver IC and a separate 802.11b transceiver IC on a single chip, such as system 100 shown in FIG. 1. Bluetooth filter and amplifier 105 and a separate wireless LAN filter and amplifier 110 are coupled to an RF transmit/receive unit 120 and a digital baseband unit 130. In the system 100, select control signal 135 is applied to switches 140 and 145 to select either the bluetooth filter and amplifier unit 105 or the wireless LAN filter and amplifier unit 110. To implement the Bluetooth unit 105 and the wireless LAN unit 110, all components for both units are fabricated and laid out separately, consuming chip area.

What is needed is a dual-mode Bluetooth/802.11b baseband circuit that can be implemented with a single IC. Preferably the resultant transceiver would provide a reconfigurable analog baseband that includes a single dual-mode complex filter system, and a single dual-mode amplifier system. Such an IC should be implementable with reduced chip area.

The present invention provides such a dual-mode analog baseband circuit.

## SUMMARY OF THE INVENTION

The present invention provides a dual-mode analog baseband circuit, implementable on a single IC. Rather than provide first and second dedicated filters for Bluetooth and for 802.11b (and/or for the related 802.11a or 802.11g standards), a single dual-mode complex filter is provided. Further, rather than provide first and second dedicated amplifiers for Bluetooth and for 802.11, a single dual-mode amplifier is provided. As a result, the analog baseband can be implemented using perhaps 60% of the area compared to using two separate baseband circuits. The resultant IC enables dual-mode receive functionality for Bluetooth and 802.11b (and/or 802.11a or 802.11g) wireless LAN.

## BRIEF DESCRIPTION OF THE FIGURES

FIG. 1 is a block diagram of a transceiver having separate Bluetooth and wireless LAN filters and amplifiers, according to the prior art.

FIG. 2 is a block diagram of a transceiver having a dual-mode filter and amplifier, according to an example of the present invention.

FIG. 3 is a high level block diagram of a dual-mode poly-phase channel-select filter and dual-mode amplifier dual-mode transceiver, according to the present invention;

FIG. 4A depicts a single stage (shown with single-ended input) of a dual-mode poly-phase five-pole Butterworth channel-select filter, according to the present invention;

FIG. 4B depicts exemplary re-configurable pole locations for a five-pole Butterworth filter comprising five cascaded single-pole stages, such as shown in Fig. 2A, to implement a dual-mode complex filter, according to the present invention;

FIG. 4C depicts the transfer function for a five-pole Butterworth dual-mode complex filter, according to the present invention; and

FIG. 5 depicts a single stage in a dual-mode amplifier system, according to the present invention.

#### 5 DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENT

A block diagram of a transceiver 200 having dual-mode functionality according to an embodiment of the present invention is shown in FIG. 2. The RF receiver/transmitter 120 is coupled to a dual-mode filter and amplifier unit 210. The dual-mode filter  
10 mode filter and amplifier unit 210 is further coupled to digital baseband unit 220. In this manner, dual-mode filter and amplifier unit 210 includes a filter operable as either a Bluetooth filter or a wireless LAN filter. The dual-mode filter includes at least one component shared by both modes. Similarly, the unit 210 includes a dual-mode amplifier unit operable as either a Bluetooth amplifier or a wireless LAN amplifier. The dual-mode amplifier includes at least one component share between both modes. Accordingly, chip area is  
15 conserved relative to the prior art embodiment of FIG. 1 through the dual-use chip components. The digital baseband unit 220 is configured to generate a mode select control signal 230 and provide this signal to the dual-mode filter and amplifier unit 210. The mode select control signal selects operation of the filter and amplifier unit 210 to filter and amplify Bluetooth signals, or wireless LAN signals, as needed.

20 While the dual-mode unit 210 is depicted in FIG. 2 as having both a dual-mode filter and a dual-mode amplifier, described further below, it is to be understood that in some embodiments, a dual-mode filter is provided coupled to separate amplifier units, one for Bluetooth operation and one for wireless LAN operation. In other embodiments, a  
25 single dual-mode amplifier is provided coupled to separate filter units, one for Bluetooth operation and one for wireless LAN operation.

As will be appreciated in the art, the dual-mode filter and amplifier unit 210 may be implemented in a wide variety of ways. Fig. 3 depicts an integrated circuit (IC) 10 that  
30 includes an embodiment of the dual-mode filter and amplifier unit 210. As described

herein, the dual-mode filter and amplifier unit 210 includes a dual-mode complex filter 30 and a dual-mode amplifier unit 40.

In receive (RX) mode, an incoming RF signal (typically 2.4 GHz) is mixed with relevant  
5 local oscillator signals (LO2-1, LO2-2) and the mixer output coupled to a dual-mode complex filter 30, according to the present invention. The local oscillator signals depend upon the RF front-end architecture of the transceiver, and present invention can be used with a range of local oscillator frequencies.

10 In Fig. 3 it is to be appreciated that in the prior art, a filter would be provided solely to filter Bluetooth compatible signals. Applicant is unaware of any prior art IC that provides the dual-mode Bluetooth/802.11 format functionality of the present invention. While this description directly addresses the Bluetooth and 802.11b configurations, it will be understood that the invention may be applied to other of the 802.11  
15 configurations such as but not limited to 802.11a, and 802.11g, as well as to other 802.11 enhancements, improvements, extensions, and standards that may be implemented in the future, as well as variants of Bluetooth and other wireless standards.

20 In Fig. 3, the signal outputs from filter 30 are coupled as input to dual-mode amplifier 40, according to the present invention. It is to be appreciated that, but for the present invention, the functionality of the dual-mode amplifier unit 40 would likely be implemented using separate amplifiers, one amplifier dedicated to filtering Bluetooth compatible signals, and a second amplifier dedicated to filtering 802.11b format  
25 compatible signals.

In practice, the chip area required to implement the filter and amplifier 210 portion of a dual-mode transceiver shown in Fig. 3 advantageously can be about 50% less than the chip area required to implement Bluetooth/802.11b functionality by combining a  
30 Bluetooth transceiver and an 802.11b transceiver on a common IC. Further, because the baseband portion 210 of IC 10 can be implemented using fewer components,

robustness and ease of fabricating IC 10 can be enhanced. As noted in Fig. 3, the RF front end of the overall dual-mode transceiver can be shared.

The dual-mode complex filter is a poly-phase channel-select filter system preferably comprising five single-pole filter units, such as shown in Fig. 4A. However for ease of illustration, the single filter stage shown in Fig. 4A is drawn with single-ended inputs, whereas in practice differential inputs can provide enhanced rejection of noise including rejection of noise from the substrate upon which IC 10 is fabricated. Those skilled in the art of filter design will know how to implement a differential input single-pole filter stage, or a cascade of five such filter stages to provide a five-pole differential dual-mode complex filter.

In Bluetooth mode (abbreviated BT in Fig. 3), dual-mode complex filter unit 30 is characterized by the five pole Butterworth configuration, whose poles are located closest to the origin in the plot shown in Fig. 4B. This fifth-order response has 1 MHz bandwidth, with complex poles located symmetrically about a 2 MHz IF, to reject a – 2MHz image. However when filter unit 30 is switched or reconfigured into 802.11 mode (abbreviated WLAN in Fig. 3), the overall Butterworth filter response is exhibited by the five poles located more distance from the origin and symmetrical about the real axis in Fig. 4B. Thus in WLAN RX mode, the complex poles are moved down to DC to perform a fifth-order Butterworth filtering on the baseband I/Q signals, with a 7.5 MHz bandwidth. The filter responses are shown in FIG. 4C. Referring back to Fig. 4A, changing the value of R2 and R3 will reposition the filter poles in the overall filter unit 30 to obtain the desired frequency responses for the dual modes of transceiver operation. Accordingly, referring back to FIG. 2, the mode control signal 230 delivered to the filter and amplifier unit 210 is operable to select the value of R2 and R3, in this embodiment, to select the mode of filter operation. Implementing filter unit 30 requires less than about 20% overhead in area for the extra resistor layout and associated switches to implement the dual functionality filter responses. This is considerably less overhead than fabricating two separate filters. Preferably a master tuning circuit corrects the IC

fabrication process and temperature variation of the RC time constant for the implementation shown in Fig. 4A.

Thus, it will be appreciated that the ability to provide a single dual-mode complex filter structure whose frequency response can be dynamically controlled to be characterized by the five-pole 802.11b configuration or by the Bluetooth configuration shown in Fig. 4B contributes to reduced chip area in implementing filtering on IC 10.

In Fig. 3, after channel-select filtering by the complex filter unit 30, signals are coupled as input to dual-mode amplifier stage 40, an exemplary stage of which is shown in Fig. 5. In Bluetooth mode, dual-mode amplifier stage 40 is configured as four-stage limiters with AC coupling, each stage exhibiting about 15 dB of gain. Resistor R1 and bias current for the associated operational amplifier set the amplitude of the limiter output, while C1 and R2 set the AC coupling high-pass corner frequency. The following third-order band-pass filter then removes the 6 MHz harmonic distortion from the limiter output. However in 802.11b WLAN mode, dual-mode amplifier stage 40 is reconfigured as a four-stage voltage gain amplifier (VGA). In this configuration mode, the total gain range is adjustable from 0 to 60 dB in 1dB increments, as set by the resistance ratio R4/R3. The VGA stages are DC-coupled, with the first stage preferably having an extra input to cancel DC offset. Advantageously the same operational amplifiers within unit 40 are used to implement the multi-state limiter Bluetooth configuration mode, and the VGA 802.11b WLAN configuration mode. Referring back to FIG. 2, the mode select control signal 230 is further operable, in this embodiment, to close the appropriate switches in FIG. 5 in each mode. In Bluetooth mode, the switches labeled BT in FIG. 5 are closed and the switches labeled  $\overline{BT}$  are open. In wireless LAN mode, the switches labeled BT are open and the switches labeled  $\overline{BT}$  are closed.

Modifications and variations may be made to the disclosed embodiments without departing from the subject and spirit of the invention as defined by the following claims.